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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 3

Complete If Known	
Application Number	10/681,759
Filing Date	October 8, 2003
First Named Inventor	Robert A. DeLine, et al.
Art Unit	2122
Examiner Name	Unknown
Attorney Docket Number	MS304376.1/MSFTP482US

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Document Number Number-Kind Code[2] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
TP ✓		US- 6,279,149	08-21-2001	Field, et al.	
TP ✓		US- 6,230,314	05-08-2001	Sweeney, et al.,	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Foreign Patent Document Country Code[3] Number[4] Kind Code[5] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T[6]

Examiner Signature	/Thai Pham/ (12/26/2006)	Date Considered	12/26/2006

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet	2	of	3

Complete if Known	
Application Number	10/681,759
Filing Date	October 8, 2003
First Named Inventor	Robert A. DeLine, et al.
Art Unit	2122
Examiner Name	Unknown
Attorney Docket Number	MS304376.1/MSFTP482US

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
TP ✓		THOMAS BALL, et al., Automatically Validating Temporal Safety Properties of Interfaces, PLDI 2001, pp. 1-10	
TP ✓		CHANDRASEKHAR BOYAPATI, et al., Ownership Types for Safe Programming: Preventing Data Races and Deadlocks, OOPSLA '02, November 4-8, 2002, Seattle, Washington	
TP ✓		WILLIAM R. BUSH, et al., A Static Analyzer for Finding Dynamic Programming Errors, Software Pracrtice & Experience, 2000, Mountain View, California	
TP ✓		MANUVIR DAS, et al., ESP: Path-Sensitive Program Verification in Polynomial Time, PLDI '02, June 17-19, 2002, pp. 57-68, Berlin, Germany	
TP ✓		ROBERT DELINE, et al., Enforcing High-Level Protocols in Low-Level Software, PLDI 2001, Snowbird, Utah	
TP ✓		ROBERT DELINE, et al., The Fugue Protocol Checker: Is Your Software Baroque?, OOPSLA '03, 2003, pp. 1-15, Anaheim, California	
TP ✓		MANUEL FAHNDRICH, et al., Adoption and Focus: Practical Linear Types for Imperative Programming, PLDI '02, June 17-19, 2002, pp. 13-24, Berlin, Germany	
TP ✓		CORMAC FLANAGAN, et al., Extended Static Checking for Java, PLDI '02, June 17-19, 2002, Berlin, Germany	
TP ✓		CORMAC FLANAGAN, et al., Type-Based Race Detection for Java, PLDI 2000, Vancouver, British Columbia, Canada	
TP ✓		JEFFREY S. FOSTER, et al., Flow-Sensitive Type Qualifiers, PLDI '02, June 17-19, 2002, pp. 1-12, Berlin, Germany	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Filing Date	October 8, 2003
First Named Inventor	Robert A. DeLine, et al.
Art Unit	2122
Examiner Name	Unknown
Attorney Docket Number	MS304376.1/MSFTP482US

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
TP ✓		ANDREW D. GORDON, et al., Typing a Multi-Language Intermediate Code, ACM, 2001	
TP ✓		SETH HALLEM, et al., A System and Language for Building System-Specific, Static Analyses, PLDI '02, June 17-19, 2002, pp. 69-82, Berlin, Germany	
TP ✓		ERIK MEIJER, et al., Technical Overview of the Common Language Runtime, 2000	
TP ✓		C. C. MICHAEL, et al., Improving Heuristic Software Analysis with Semantic Information, 2001, pp. 1-8	
TP ✓		JOHN PENIX, REBOUND: A Framework for Automated Component Adaptation, 1998	
TP ✓		SANDER TICHELAAR, et al., A Meta-Model for Language-Independent Refactoring, Proceedings ISPSE 2000, IEEE, 2000, pp. 157-167	

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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet	1	of	1

	Complete If Known
Application Number	10/681,759
Filing Date	October 8, 2003
First Named Inventor	Robert A. DeLine
Art Unit	2122
Examiner Name	Unknown
Attorney Docket Number	MS304376.01/MSFTP482US

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TP ✓		KARL CRARY et al., Typed Memory Management in a Calculus of Capabilities, Conference Record of the 26th Annual ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages, January 1999, pp. 262-275, ACM Press.	
TP ✓		Proceedings of the ACM SIGPLAN 2002 Conference on Programming Language Design and Implementation, June 17-19, 2002, 326 pages.	
TP ✓		ROBERT E. STROM et al., Typestate: A Programming Language Concept for Enhancing Software Reliability, IEEE Transaciotns on Software Engineering, January 1986, pp. 157-171, Vol. 12, Issue 1.	

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